energynetworks association

Form A2-3: Compliance Verification Report for Inverter Connected Power Generating Modules

This form should be used by the **Manufacturer** to demonstrate and declare compliance with the requirements of EREC G99.The form can be used in a variety of ways as detailed below:

1.To obtain **Fully Type Tested** status

The **Manufacturer** can use this form to obtain **Fully Type Tested** status for a **Power Generating Module** by registering this completed form with the Energy Networks Association (ENA) Type Test Verification Report Register.

2.To obtain **Type Tested** status for a product

This form can be used by the **Manufacturer** to obtain **Type Tested** status for a productwhich is used in a **Power Generating Module** by registering this form with the relevant parts completed with the Energy Networks Association (ENA) Type Test Verification Report Register.

3. One-off Installation

This form can be used by the **Manufacturer** or **Installer** to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99.This form must be submitted to the **DNO** as part of the application.

A combination of (2) and (3) can be used as required, together with Form A2-4 where compliance of the **Interface Protection** is to be demonstrated on site.

Note:

Within this Form A2-3 the term **Power Park Module** will be used but its meaning can be interpreted within Form A2-3 to mean **Power Park Module**, **Generating Unit or Inverter** as appropriate for the context. However, note that compliance must be demonstrated at the **Power Park Module** level.

If the **Power Generating Module** is **FullyType Tested** and registered with the Energy Networks Association (ENA) Type Test Verification Report Register, the Installation Document (Form A3-1 or A3-2) should include the **Manufacturer's** reference number (the Product ID), and this form does not need to be submitted.

Where the **Power Generating Module** is not registered with the ENA Type Test Verification Report Register or is not **Fully Type Tested** this form (all or in parts as applicable) needs to be completed and provided to the **DNO**, to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99.

Manufacturer's reference number		DQ2010019-01	
PGM technology		Solis-255K-EHV-5G, Solis-250K-EHV-5G, Solis-208K-EHV-5G, Solis-255K-EHV-5G-PLUS, Solis-250K-EHV-5G-PLUS, Solis-215K-EHV-5G-PLUS,	
Manufacturer name		Ginlong Technologies Co., Ltd.	
Address		No. 57 Jintong Road, Seafront (Binhai) Industrial Park, Xiangshan, Ningbo, Zhejiang, 315712,P.R.China	
Tel	(+86) 574 6580 3377	Web site	www.ginlong.com
E:mail	jiaqi.cao@ginlong.com		
Registered Capacity		255KVA(Solis-255K-EHV-5G,Solis-255K-EHV-5G-PLUS)	

	250KVA(Solis-250K-EHV-5G,Solis-250K-EHV-5G-PLUS) 215KVA(Solis-215K-EHV-5G-PLUS) 208KVA(Solis-208K-EHV-5G)

There are four options for Testing: (1) **Fully Type Tested**, (2) Partially **Type Tested**, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options.With the exception of**Fully Type TestedPGMs** tests marked with * may be carried out at the time of commissioning (Form A4).

Tested option:	**1. Fully Type Tested**	**2.Partially Type Tested**	**3. One-off Man. Info.**	**4. Tested on Site at time of Commission-ing**
0. **Fully Type Tested-** all tests detailed below completed and evidence attached to this submission	**Yes**	**N/A**	**N/A**	**N/A**
1. Operating Range	**N/A**			
2. PQ – Harmonics				
3. PQ – Voltage Fluctuation and Flicker				
4. PQ – DC Injection (**Power Park Modules** only)				
5. **Power Factor** (PF)*				
6. Frequency protection tripand ride through tests*				
7. Voltageprotectiontrip and ride through tests*				
8. Protection – Loss of Mains Test*, Vector ShiftandRoCoF Stability Test*				
9.**LFSM-O** Test*				
10. Protection – Reconnection Timer*				
11. Fault Level Contribution				
12. Self-monitoring Solid State Switch				
13. Wiring functional tests if required by para 15.2.1 (attach relevant schedule of tests)*				
14. Logic Interface (input port)*				

* may be carried out at the time of commissioning (Form A.2-4).

Document reference(s) for **Manufacturers' Information:**

Manufacturer compliance declaration. - I certify that all products supplied by the company with the above**TypeTestedManufacturer's** reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site **Modifications** are required to ensure that the product meets all the requirements of EREC G99.			
Signed	Caojiaqi	On behalf of Manufacturerstamp	锦浪科技股份有限公司 GINLONG TECHNOLOGIES CO.,LTD.
Note that testing can be done by the **Manufacturer** of an individual component or by an external test house. Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.			

A2-3 Compliance Verification Report –Tests for Type A Inverter Connected Power Generating Modules – test record	
1. Operating Range: Two tests should be carried with the **Power Generating Module** operating at **RegisteredCapacity** and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within ± 5 % of the apparent power value set for the entire duration of each test sequence. Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module**can operate within the required ranges for the specified period of time. The **Interface Protection** shall be disabled during the tests. In case of a PV **Power Park Module** the PV primary source may be replaced by a DC source. In case of a full converter **Power ParkModule**(eg wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a DC source.	
Test 1 Voltage = 85% of nominal (680 V) Frequency = 47 Hz, **Power Factor** = 1, Period of test 20s	Tested with the specified conditions,in the 20 seconds period of time,the inverters operate normally
Test 2 Voltage = 85% of nominal (680 V) Frequency = 47.5 Hz, **Power Factor** = 1, Period of test 90 minutes	Tested with the specified conditions,in the 90 minutes period of time,the inverters operate normally
Test 3 Voltage = 110% of nominal (880 V) Frequency = 51.5 Hz, **Power Factor** = 1, Period of test 90 minutes	Tested with the specified conditions,in the 90 minutes period of time,the inverters operate normally
Test 4 Voltage = 110% of nominal (880 V) Frequency = 52.0 Hz, **Power Factor** = 1, Period of test 15 minutes	Tested with the specified conditions,in the 15 minutes period of time,the inverters operate normally
Test 5 RoCoF withstand Confirm that the **Power Generating Module** iscapable of staying connected to the **DistributionNetwork** and operate at rates of change offrequency up to 1 Hzs^{-1} as measured over aperiod of 500 ms. Note that this is not expectedto be demonstrated on site.	Tested with the specified conditions, the inverters operate normally

2. Power Quality – Harmonics:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5.These tests should be carried out as specified in BS EN 61000-3-12The results need to comply with the limits of Table2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment.
Power Generating Modules with emissions close to the limits laid down in BS EN 61000-3-12 may require the installation of a transformer between 2 and 4 times the rating of the **Power Generating Module**in order to accept the connection to a **Distribution Network.**
For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC G5.

Power Generating Module tested to BS EREC G5

Power Generating Module rating per phase (rpp)			85	kVA	Harmonic % = Measured Value (A) x 23/rating per phase (kVA)
Harmonic	At 45-55% of **Registered Capacity**		100% of **Registered Capacity**		Limit in BS EREC G5
	Measured Value MV in Amps	%	Measured Value MV in Amps	%	
Phase1					
2	0.693	0.187	0.707	0.191	1.6%
3	0.138	0.037	0.179	0.048	4%
4	0.905	0.245	0.858	0.232	1.0%
5	0.242	0.065	1.557	0.421	4%
6	0.072	0.020	0.079	0.021	0.5%
7	0.607	0.164	1.997	0.540	4%
8	0.386	0.105	0.443	0.120	0.4%
9	0.079	0.021	0.147	0.040	1.2%
10	0.322	0.087	0.291	0.079	0.4%
11	0.794	0.215	0.361	0.098	3%
12	0.067	0.018	0.083	0.022	0.2
13	0.374	0.101	0.899	0.243	2.5%
14	0.277	0.075	0.399	0.108	0.2%
15	0.086	0.023	0.145	0.039	0.3%
16	0.236	0.064	0.192	0.052	0.2%

17	0.202	0.055	0.464	0.126	1.6%
18	0.075	0.020	0.095	0.026	0.2%
19	0.536	0.145	0.549	0.149	1.2%
20	0.212	0.057	0.328	0.089	0.2%
21	0.101	0.027	0.177	0.048	0.2%
22	0.215	0.058	0.183	0.050	0.2%
23	0.465	0.126	0.484	0.131	1.2%
24	0.081	0.022	0.100	0.027	0.2%
25	0.184	0.050	0.513	0.139	0.7%
THD[1]	---	2.128	---	1.802	5%
Phase2					
2	1.039	0.281	0.910	0.246	1.6%
3	0.101	0.027	0.121	0.033	4%
4	0.876	0.237	0.858	0.232	1.0%
5	0.284	0.077	1.488	0.403	4%
6	0.093	0.025	0.080	0.022	0.5%
7	0.608	0.165	1.910	0.517	4%
8	0.357	0.096	0.440	0.119	0.4%
9	0.062	0.017	0.132	0.036	1.2%
10	0.300	0.081	0.269	0.073	0.4%
11	0.773	0.209	0.329	0.089	3%
12	0.090	0.024	0.098	0.026	0.2
13	0.352	0.095	0.877	0.237	2.5%
14	0.267	0.072	0.394	0.107	0.2%
15	0.072	0.020	0.144	0.039	0.3%

[1] THD = Total Harmonic Distortion

16	0.257	0.069	0.194	0.052	0.2%
17	0.221	0.060	0.497	0.134	1.6%
18	0.089	0.024	0.115	0.031	0.2%
19	0.517	0.140	0.501	0.136	1.2%
20	0.224	0.061	0.328	0.089	0.2%
21	0.105	0.029	0.180	0.049	0.2%
22	0.259	0.070	0.209	0.056	0.2%
23	0.456	0.123	0.492	0.133	1.2%
24	0.108	0.029	0.121	0.033	0.2%
25	0.171	0.046	0.498	0.135	0.7%
THD[2]	---	2.258	---	1.775	5%
Phase3					
2	0.706	0.191	0.750	0.203	1.6%
3	0.159	0.043	0.187	0.051	4%
4	0.832	0.225	0.805	0.218	1.0%
5	0.337	0.091	1.473	0.399	4%
6	0.093	0.025	0.083	0.022	0.5%
7	0.663	0.180	1.874	0.507	4%
8	0.381	0.103	0.469	0.127	0.4%
9	0.074	0.020	0.093	0.025	1.2%
10	0.329	0.089	0.282	0.076	0.4%
11	0.795	0.215	0.248	0.067	3%
12	0.0949	0.026	0.085	0.023	0.2
13	0.404	0.109	0.911	0.246	2.5%
14	0.230	0.062	0.382	0.103	0.2%

[2] THD = Total Harmonic Distortion

15	0.069	0.019	0.143	0.039	0.3%
16	0.249	0.067	0.214	0.058	0.2%
17	0.207	0.056	0.465	0.126	1.6%
18	0.091	0.025	0.095	0.026	0.2%
19	0.554	0.150	0.538	0.146	1.2%
20	0.234	0.063	0.318	0.086	0.2%
21	0.080	0.022	0.169	0.046	0.2%
22	0.218	0.059	0.211	0.057	0.2%
23	0.384	0.104	0.475	0.128	1.2%
24	0.096	0.026	0.099	0.027	0.2%
25	0.187	0.051	0.505	0.137	0.7%
THD[3]	---	2.127	---	1.729	5%

3. Power Quality – Voltage fluctuations and Flicker:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC P28.

	Starting			Stopping			Running	
	d max	d c	d(t)	d max	d c	d(t)	P st	P lt 2 hours
Measured Values at test impedance	0.090%	0.003%	0.01%	0.094%	0.004 %	0.01%	0.109	0.074
Normalised to standard impedance	0.090%	0.003%	0.01%	0.094%	0.004 %	0.01%	0.109	0.074
Measured Values of Pashe B at test impedance	0.162%	0.005%	0.01%	0.168%	0.005 %	0.01%	0.108	0.070

[3] THD = Total Harmonic Distortion

Normalised to standard impedance	0.162%	0.005%	0.01%	0.168%	0.005 %	0.01%	0.108	0.070
Measured Values of Pashe B at test impedance	0.105%	0.007%	0.01%	0.108%	0.007 %	0.01%	0.104	0.070
Normalised to required maximum impedance	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A
Limits set under BS EN 61000-3-11	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65

Test Impedance	R	0.4	Ω	XI	0.15	Ω
Standard Impedance	R	0.24 * 0.4 ^	Ω	XI	0.15 * 0.25 ^	Ω
Maximum Impedance	R	N/A	Ω	XI	N/A	Ω

* Applies to three phase and split single phase **Power Generating Modules.**

^ Applies to single phase **Power Generating Module** and **Power Generating Modules** using two phases on a three phase system

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the **Power Factor** of the generation output is 0.98 or above.

Normalised value = Measured value x reference source resistance/measured source resistance at test point

Single phase units reference source resistance is 0.4 Ω

Two phase units in a three phase system reference source resistance is 0.4 Ω

Two phase units in a split phase systemreference source resistance is 0.24 Ω

Three phase units reference source resistance is 0.24 Ω

Where the **Power Factor** of the output is under 0.98 then the XI to R ratio of the test impedance should be close to that of the Standard Impedance.

The stopping test should be a trip from full load operation.

The duration of these tests need to comply with the particular requirements set out in the testing notes for the technology under test.Dates and location of the test need to be noted below

Test start date	15. Oct.2020	Test end date	19. Oct.2020
Test location	Ginlong Technologies Co.,Ltd.		

4.Power quality – DC injection: The tests should be carried out on a single **Generating Unit**. Tests are to

be carried out at three defined power levels ±5%. At 800V a 255kW single phase **Inverter** has a current output of 184A so DC limit is460mA. These tests should be undertaken in accordance with Annex A.7.1.4.4.

Test power level	10%			55%			100%		
	L1	L2	L3	L1	L2	L3	L1	L2	L3
Recorded value in Amps (mA)	34.50	39.51	52.0	190.78	49.79	176.12	358.26	67.51	283.55
as % of rated AC current	0.019	0.021	0.028	0.104	0.027	0.096	0.195	0.037	0.154
Limit	0.25%			0.25%			0.25%		

5.Power Factor: The tests should be carried out on a single **Power Generating Module**. Tests are to be carried out at three voltage levels and at **Registered Capacity**. Voltage to be maintained within ±1.5% of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2.

Voltage	0.94 pu	1.0 pu	1.1 pu
Measured value	0.999	0.999	0.999
Power Factor Limit	>0.95	>0.95	>0.95

6. Protection – Frequency tests: Those tests should be carried out in accordance with the Annex A.7.1.2.3.

Function	Setting		Trip test		"No trip tests"	
	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F stage 1	47.5Hz	20s	47.49Hz	20.100s	47.7Hz 30s	Yes
U/F stage 2	47Hz	0.5s	47.01Hz	0.568s	47.2Hz 19.5s	Yes
					46.8Hz 0.45s	Yes
O/F	52Hz	0.5s	51.98Hz	0.510s	51.8Hz 120s	Yes
					52.2Hz 0.45s	Yes

Note. For frequency trip tests the frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

7. Protection – Voltage tests: Those tests should be carried out in accordance with Annex A.7.1.2.2.						
Function	Setting		Trip test		"No trip tests"	
U/V	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
L1-L2 L2-L3 L3-L1	0.8 pu (640V)	2.5s	638.2V 638.5V 638.8V	2.581s	644V 5s	Yes
L1-L2 L2-L3 L3-L1			639.2V 638.7V 638.9V	2.564s		Yes
L1-L2 L2-L3 L3-L1			638.5V 638.8V 638.4V	2.556s		Yes
					636V 2.45s	Yes
O/V stage1	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
L1-L2 L2-L3 L3-L1	1.14pu (912V)	1.0s	911.2V 911.4V 910.8V	1.057s	908V 5.0s	Yes
L1-L2 L2-L3 L3-L1			911.8V 911.5V 911.1V	1.064s		Yes
L1-L2 L2-L3 L3-L1			911.2V 911.4V 910.8V	1.052s		Yes
O/V stage 2	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
L1-L2 L2-L3 L3-L1	1.19pu (952V)	0.5s	952.5V 952.2V 952.6V	0.563s	948V 0.95s	Yes

L1-L2 L2-L3 L3-L1			952.1V 952.2V 951.8V	0.559s		Yes
L1-L2 L2-L3 L3-L1	1.19pu (952V)	0.5s	951.9V 951.6V 951.4V	0.554s	948V 0.95s	Yes
					956V 0.45s	Yes

Note for Voltage tests the Voltage required to trip is the setting ±3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection.The No trip tests need to be carried out at the setting ±4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

8.Protection – Loss of Mains test: Those tests should be carried out in accordance with BS EN 62116. Annex A.7.1.2.4.

The following sub set of tests should be recorded in the following table.

Test Power and imbalance	33% -5% Q Test 22	66% -5% Q Test 12	100% -5% P Test 5	33% +5% Q Test 31	66% +5% Q Test 21	100% +5% P Test 10
Trip time. Limit is 0.5s	0.223s	0.348s	0.366s	0.210s	0.363s	0.361s

Loss of Mains Protection, Vector Shift Stability test. This test should be carried out in accordance with Annex A.7.1.2.6.

	Start Frequency	Change	Confirm no trip
Positive Vector Shift	49.5Hz	+50 degrees	Yes
Negative Vector Shift	50.5Hz	- 50 degrees	Yes

Loss of Mains Protection, RoCoF Stability test: This test should be carried out in accordance with Annex A.7.1.2.6.

Ramp range	Test frequency ramp:	Test Duration	Confirm no trip
49.0Hz to 51.0Hz	+0.95Hzs^{-1}	2.1 s	Yes
51.0Hz to 49.0Hz	-0.95Hzs^{-1}	2.1 s	Yes

9. Limited Frequency Sensitive Mode – Over frequency test: The test is using the specific threshold frequency of 50.4 Hz and Droop of5%.

This test should be carried out in accordance with Annex A.7.1.3.

Active Power response to rising frequency/time plots are attached if frequency injection tests are undertaken in accordance with Annex A.7.2.4.	**Yes**

Alternatively, simulation results should be noted below:

Test sequence at **Registered Capacity**>80%	Measured **Active Power** Output	Frequency	Primary Power Source	**Active Power** Gradient
Step a) 50.00Hz ±0.01Hz	254.70kW	50.00Hz	259.08kW	-
Step b) 50.45Hz ±0.05Hz	249.40kW	50.45Hz		-
Step c) 50.70Hz ±0.10Hz	224.00kW	50.70Hz		-
Step d) 51.15Hz ±0.05Hz	178.10kW	51.15Hz		-
Step e) 50.70Hz ±0.10Hz	223.70kW	50.70Hz		-
Step f) 50.45Hz ±0.05Hz	243.92kW	50.45Hz		-
Step g) 50.00Hz ±0.01Hz	255.0kW	50.00Hz		1530kW/min
Test sequence at **Registered Capacity** 40% - 60%	Measured **Active Power** Output	Frequency	Primary Power Source	**Active Power** Gradient
Step a) 50.00Hz ±0.01Hz	129.58kW	50.00Hz	131.53kW	-
Step b) 50.45Hz ±0.05Hz	124.23kW	50.45Hz		-
Step c) 50.70Hz ±0.10Hz	98.72kW	50.70Hz		-
Step d) 51.15Hz ±0.05Hz	52.85kW	51.15Hz		-
Step e) 50.70Hz ±0.10Hz	98.81kW	50.70Hz		-
Step f) 50.45 Hz ±0.05 Hz	124.24kW	50.45Hz	131.53kW	-
Step g) 50.00 Hz ±0.01 Hz	124.24kW	50.00Hz	131.53kW	1530kW/min

10. Protection – Re-connection timer.

Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 10.1.

Time delay setting	Measured delay	Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of Table 10.1.			
30s	45.8s	At 1.16 pu	At 0.78 pu	At 47.4 Hz	At 52.1 Hz
Confirmation that the **Power Generating Module** does not re-connect.		Yes	Yes	Yes	Yes

11. Fault level contribution: Those tests shall be carried out in accordance with EREC G99 Annex A.7.1.5.		
For **Inverter** output		
Time after fault	Volts	Amps
20ms	56.9V	184.1A
100ms	47.8V	42.5A
250ms	21.5V	16.72A
500ms	0V	8.12A
Time to trip	0.398s	In seconds
12. Self-Monitoring solid state switching: No specified test requirements.Referto Annex A.7.1.7.		
It has been verified that in the event of the solid state switching device failing to disconnect the **Power Park Module**, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 s.	N/A (Solid state switch means electronic switch, Solis inverter uses mechanical dual relay protection with relay checks, which drops the voltage below 50V in 0.5s)	
13. Wiring functional tests: If required by para 15.2.1.		
Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning)	N/A(Not applicable. Refer to 15.2.1, inverter is using special connector for wiring)	
14. Logic interface (input port).		
Confirm that an input port is provided and can be used to shut down the module.	Yes (Logic interface is marked as "DRM" either on inverter or on external DRM device depending on inverter model. Please see inverter or external DRM device manual for detail.	
Additional comments.		